

14bit A/D converter ISP
Optional, currently not

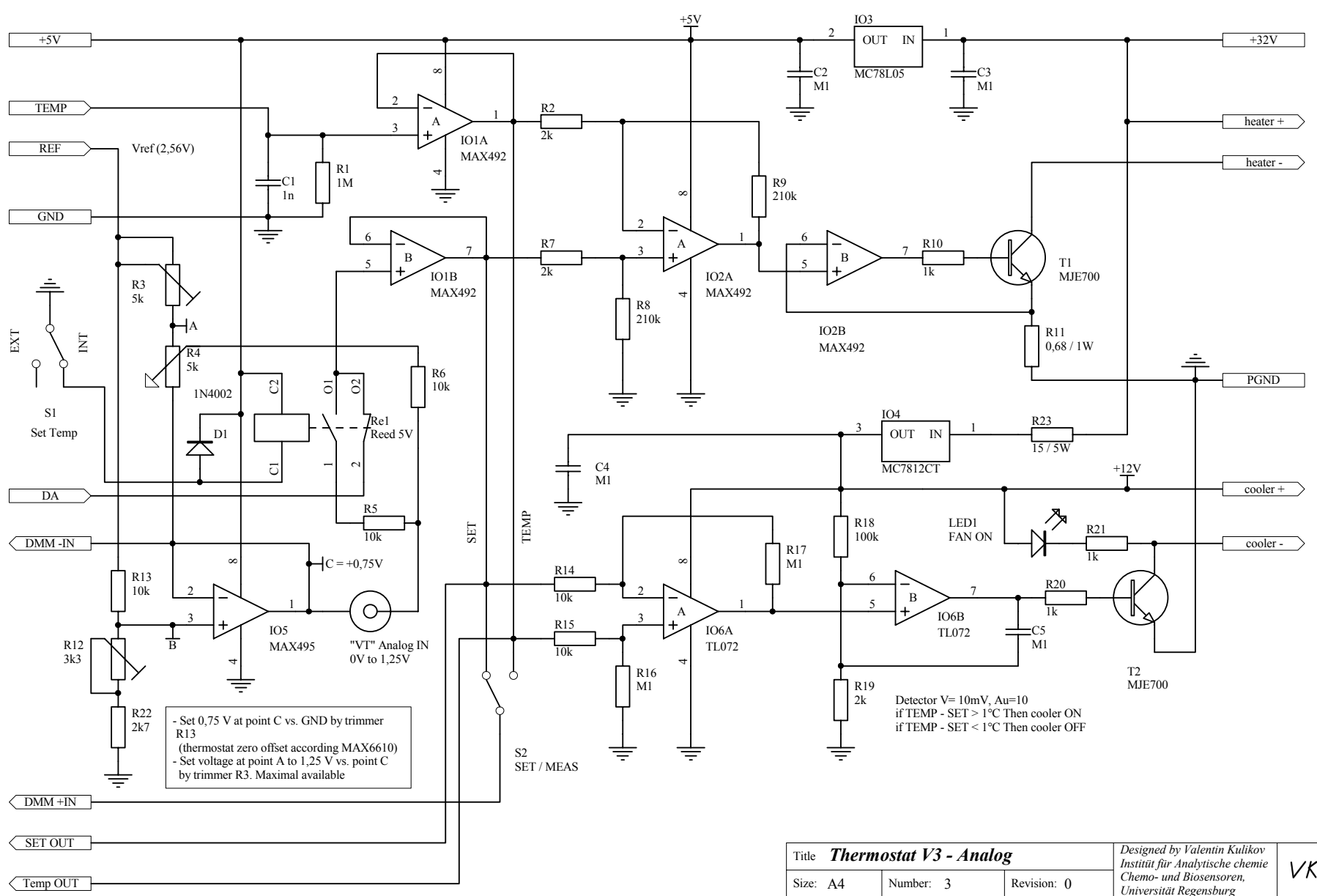
12bit D/A converter ISP (equiv. MAX538)
Voff (D=0, zero) = 0,5 mV
Vref (D=4096, max) = 2043-48 mV

HEAT: heating foil app. 750hm
COOL: PC FAN
SENSE: Analog temp. sensor

DMM
Range 200 mV
3 3/4 DIGIT LCD
HED282T

1:10 Divider (RA, RB)
Ra=243k (0,1%)
Rb=13,7k + 13,3k (0,1%)

Title Thermostat V3 (complete)			Designed by Valentin Kulikov Institut für Analytische chemie Chemo- und Biosensoren, Universität Regensburg e-mail: kulikov@elf.stuba.sk	VKU 2003
Size: A4	Number: 1	Revision: 0		
Updated: 13-Sep-2003	Time: 18:21:43	Sheet 2 of 2		
File: Main.Sch				



Title Thermostat V3 - Analog		Designed by Valentin Kulikov Institut für Analytische chemie Chemo- und Biosensoren, Universität Regensburg e-mail: kulikov@elf.stuba.sk	
Size: A4	Number: 3	Revision: 0	VKU 2003
Updated: 13-Sep-2003	Time: 18:21:43	Sheet 1 of 2	
File: thermostat.Sch			